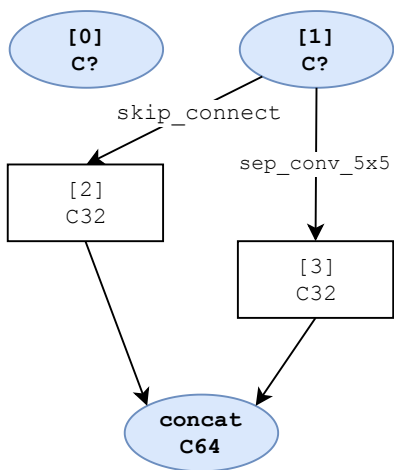


```

cell {
  id: 0
  channel: 32
  num_steps: 4
  type: NORMAL
  op: { frm: 0; to: 2; type: SKIP_CONNECT }
  op: { frm: 1; to: 2; type: DIL_CONV_3X3 }
  op: { frm: 0; to: 3; type: SKIP_CONNECT }
  op: { frm: 1; to: 3; type: SEP_CONV_3X3 }
  op: { frm: 1; to: 4; type: SEP_CONV_3X3 }
  op: { frm: 3; to: 4; type: SEP_CONV_3X3 }
  op: { frm: 0; to: 5; type: SEP_CONV_3X3 }
  op: { frm: 4; to: 5; type: DIL_CONV_5X5 }
  concat: 2
  concat: 3
  concat: 4
  concat: 5
}

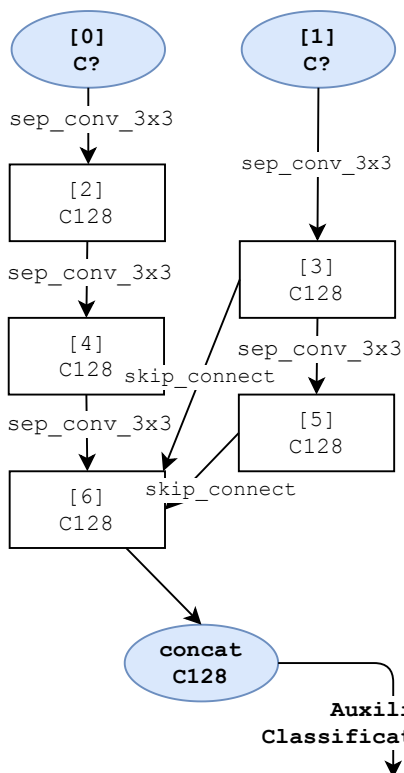
```



```

cell {
  id: 1
  channel: 32
  num_steps: 2
  type: REDUCE
  op: { frm: 1; to: 2; type: SKIP_CONNECT }
  op: { frm: 1; to: 3; type: SEP_CONV_5X5 }
  concat: 2
  concat: 3
}

```



```

cell {
  id: 2
  channel: 128
  num_steps: 5
  type: NORMAL
  op: { frm: 0; to: 2; type: SEP_CONV_3X3 }
  op: { frm: 1; to: 3; type: SEP_CONV_3X3 }
  op: { frm: 2; to: 4; type: SEP_CONV_3X3 }
  op: { frm: 3; to: 5; type: SEP_CONV_3X3 }
  op: { frm: 4; to: 6; type: SEP_CONV_3X3 }
  op: { frm: 3; to: 6; type: SKIP_CONNECT }
  op: { frm: 5; to: 6; type: SKIP_CONNECT }
  concat: 6
  auxiliary: True
}

```